

# 2018 DASC Report

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**Design Automation Standards Committee**  
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# Introduction to the DASC

- One of the oldest sponsor groups
  - founded in 1987 as the Design Automation Standards Subcommittee
- Sponsors standards in the Electronic Design Automation Area
  - SW fo Electronics Design
- 21 companies in the Electronics industry represented
- Meets one a month, once per year Face to Face

# 2017-2018 Highlights (1)

- 1801.2 standard (UVM) passed
  - Used in verifying close to 70% of electronic System on Chip designs
- 1076.1 standard (VHDL AMS) passed
  - Original hardware description language (HDL) for Digital-Analog designs
- 1800 standard (SystemVerilog) approved
  - Now arguably the HDL of choice world wide

## 2017-2018 Highlights (2)

- P1801 WG (Low Power) Ballot successful. In reconciliation phase
- P1735 WG (IP Encryption) WG PAR revision approved
- P2401 (LSI-Package-Board) WG PAR revision approved
- P1666 (SystemC) PAR approved
- Extensions for P2415 and P2416 (Low Power) approved by Nescom

# 2017-2018 Lowlights

- P1076 (VHDL) had been struggling
  - Technical writing was done on a shoestring, but now appears to be on track

# DASC and Other Standards Groups

- Continued cooperation with Accellera
  - 10 year "Get IEEE" contract funding
- Increased cooperation with Si2
  - Contributions from Si2 to P1801, P2416 and potentially to P2401
- Ongoing cooperation with JEITA
  - JEITA reps have joined DASC F2F meetings at DVCon for several years
  - DASC-JEITA joint meeting in Japan in November is now ongoing for 10 years

# Current Published DASC Standards

IEEE Standard #	Approved	IEC Dual Logo
1076 VHDL	2008	D
1076.1 (VHDL AMS)	2017	D
1481 (OLA)	2009	D
1492 (SDF)	2010	D
1647 (e language)	2016	
1666 (SystemC)	2011	D
1666.1 (SystemC AMS)	2016	D
1685 (IP-XACT)	2014	D
1734 (Quality IP)	2011	D
1735 (IP Encryption)	2015	
1800 (SystemVerilog)	2017	D
1800.2 (UVM)	2017	D
1801 (UPF)	2015	D
1850 (PSL)	2010	D
2401 (LSI Package Board)	2015	D

# New DASC Standards in Development

IEEE PAR #	PAR Approved	Ballot Expected
P2415 (UHA)	2014	2019
P2416 (System Level power Modeling)	2014	2019

\* PAR extension  
^ in second Ballot



# DASC Standards Being Revised

IEEE PAR #	PAR Approved	Ballot Expected
P1076 (VHDL)	2017	2018
P1647 (e)	2017	2020
P1666 (SystemC)	2018	2021
P1735 (IP encryption)	2018	2020
P1801 (UPF)	2016	2018*
P2401 (LSI Package Board)	2018	2020

\* in resolution phase