

DASC Report

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Design Automation Standards
Committee
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DASC Activity

- Monthly meeting schedule maintained
 - Core members (~20) attend meetings
 - 15 members typically do not attend, but maintain DASC membership
- Joint meeting with JEITA to be held in November 2016
- Policies and Procedure updates ongoing
 - P1801 and 1647 update approved
 - P1076 & 1800 updates in (or nearly in) Ballot

Current DASC Standards

IEEE Standard #	Approved	IEC Dual Logo
1076 VHDL	2008	D
1076.1 (VHDL AMS)	2009	D
1076.1,1 (VHDL-AMS Extensions)	2011	
1481 (OLA)	2009	D
1492 (SDF)	2010	D
1647 (e language)	2016	
1666 (SystemC)	2011	D
1666.1 (SystemC AMS)	2016	T
1685 (IP-XACT)	2014	D
1734 (Quality IP)	2011	D
1735 (IP Encryption)	2015	
1800 (SystemVerilog)	2012	D
1801 (UPF)	2015	D/T
1850 (PSL)	2010	D
2401 (LSI Package Board)	2015	D

D = dual logo

T = Will be proposed as Dual Standard

Active DASC Working Groups

IEEE PAR #	PAR Approved	Ballot Expected
P1076 (VHDL)	2015*	2017
P1076.1 (VHDL-AMS)	2015*	2017
P1800 (SystemVerilog)	2016	2018
P1800.2 (UVM)	2014^	201
P2415 (UHA)	2014	2017
P2416 (System Level power Modeling)	2014	2017

* PAR extension
^ in second Ballot

New/Revised PARs in 2017/2018 (Potential)

IEEE PAR # (if known)	PAR Type
1647 (e language)	Revision
1666 (SystemC)	Revision
1666.2 (UVM SystemC)	New standard (outside chance that this will happen)
1666.3 (SystemC Synthesis)	New Standard
1685 (IP-XACT)	Revision
1800 (SystemVerilog)	Revision (more substantial than current "bug fix" update)
1800.1 (SystemVerilog AMS)	New standard
1801 (UPF)	Revision (likely a "bug fix")
2401 (LPB)	Revision
2401.1 LPB System Extensions	New Standard