



# 1076.1 (VHDL-AMS)

## Status Report

San Diego 11 June 2004

- ◆ 1076.1 due for re-ballot this year
  - Revision PAR to be submitted by end of June 04
- ◆ Mandatory revisions will be proposed
  - Corrections to LRM documented in errata sheet
  - Changes in light of VHDL 200X
- ◆ Non-mandatory revisions will be considered
  - SPICE netlist interface
  - Mixed nets
  - Frequency domain review