

# VHDL / 1076 General Status

- Officers last elected on Dec 16, 2005, no elections due.
  - Chair: Jim Lewis
  - Vice Chair: Chuck Swart
  - Secretary: Peter Ashenden
- P&Ps approved on: 4-Nov-2005
  - Posted at:  
[http://www.eda.org/vasg/meetings/VASG\\_Operating\\_Procedures.pdf](http://www.eda.org/vasg/meetings/VASG_Operating_Procedures.pdf)
- IEEE WG active
  - Supporting current balloting and ISAC issue voting
  - voting records at: <http://www.eda.org/vasg/voting/>
  - Expect active VHDL development during the next several years

# VHDL / 1076 Par Status

- Currently we have 2 PARs open.
  - Original plan was to bring VHPI and VHDL-200X revisions to IEEE standards ballot at the same time.
  - Since VHDL-200X revisions were delayed by funding issues, decided to do VHPI as a separate amendment PAR.
- Amendment Par: P1076C = VHDL + VHPI.
  - Par expires 31-Dec-2010
  - Balloting completed on 2-Nov-2006: Passed
  - Currently preparing RevCom submission
- Revision Par: P1076 = P1076C + VHDL-200X + Accellera revisions
  - Par expires 31-Dec-2008
  - Expect to prepare Accellera VHDL-2006 3.0 standard for IEEE ballot in June/July 2007.

# VHDL / 1076 Relationships

- Collaborating with Accellera VHDL TSC to develop revisions
  - MOU at: <http://www.eda.org/vasg/meetings/AccelleraVASGmouV7Approved.pdf>
  - Relationship details at: <http://www.eda.org/vasg>
  - Accellera VHDL TSC is actively working on VHDL updates
  - This relationship is necessary to solve LRM development funding issues
- Alex Z has reminded us that VHDL is a dual-logo standard with IEC and that we should be engaging IEC TC 93 in the development process
  - PAR P1076C (VHPI) work predates the dual-logo
  - PAR P1076 and dual-logo standard approved during same year
  - This is a work in progress
    - I hope to bring IEC into the Accellera process
- Dual Logo standards issues
  - The VHDL standards are well known as 1076, 1076.6, ...
  - Havoc has resulted since IEEE-SA now references these by the IEC number and has not adequately cross-referenced them

# VHDL / 1076 Status

- Working Group Roster (VASG voting members)
  - Posted at: [http://www.eda.org/vasg/voting/vasg\\_roster.pdf](http://www.eda.org/vasg/voting/vasg_roster.pdf)

<u>Last Name</u>	<u>First Name</u>	<u>Affiliation</u>	<u>DASC</u>	<u>Vote in 2 of 3</u>
Ashenden	Peter	Ashenden Designs	2006	Yes
Aynsley	John	Doulos	2006	Yes
Bailey	Stephen	Mentor Graphics	2006	Yes
Lewis	Jim	SynthWorks	2006	Yes
Molenkamp	Bert	University of Twente	2006	Yes
Myers	Robert	Raytheon	2006	Yes
Ries	John	Mentor Graphics	2006	Yes
Shields	John	Mentor Graphics	2006	Yes
Swart	Chuck	Mentor Graphics	2006	Yes
Varikat	Ajayharsh	Cadence Design Systems	2006	Yes
Willis	John	FTL Systems	2006	Yes
Zwolinski	Mark	Univ. of Southampton	2006	Yes